

N-Channel SGT Power MOSFET

Features

- N-channel
- $V_{DS} = 100V$, $I_D = 100A$
 $R_{DS(ON)} < 8m\Omega @ V_{GS} = 10V$ (Typ:6.3m Ω)
- 100% avalanche tested
- Pb-free lead plating; RoHS compliant

Application

- High performance SMPS, e.g. sync. rec.
- Hard Switching and High Speed Circuit
- Motor Control

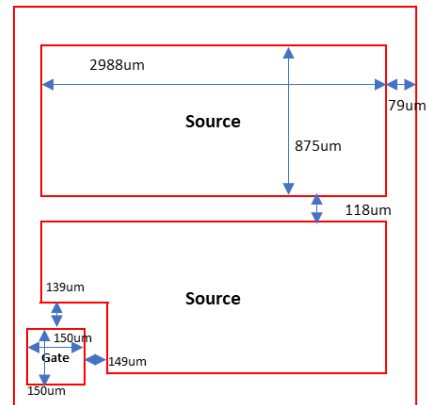
Wafer Size (inch)	8
Chip Size without scribe (mm)	3.14x2.01
Wafer Thickness (mil)	8
Top Metal	AlCu
Top Metal Thickness (μm)	4
Back Metal	Ti/Ni/Ag
Scribe Line (μm)	60
Gate Wire recommended	1*1.5mil Cu Wire
Source Wires recommended	15x 2mil Al Ribbon
Gross Die	4328

100V N-Ch Power MOSFET

Parameter	Value	Unit
V_{DS}	100	V
$R_{DS(on),typ}$ $V_{GS} = 10V$	6.3	m Ω
I_D	100	A

Unit: μm

Die Size Without 60 μm scribe line



Source Pad size:2988um*870um

Gate Pad size:150um*150um

Electrical Characteristics at T_j=25°C (unless otherwise specified)
Static Characteristics

Parameter	Symbol	Test Condition	Value			Unit
			Min.	Typ.	Max.	
Drain to Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	100	108		V
Gate Threshold Voltage	V _{GS(th)}	V _{GS} =V _{DS} , I _D =250μA	1.2	1.7	2.2	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} =0V, V _{DS} =100V, T _j =25°C	-	0.01	1	μA
		V _{GS} =0V, V _{DS} =100V, T _j =100°C		-	100	
Gate to Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	1.4	±100	nA
Drain to Source on Resistance	R _{DS(on)}	V _{GS} =10V, I _D =20A	-	6.3	8.0	mΩ
		V _{GS} =4.5V, I _D =20A	-	8.6	11	
Gate Resistance	R _G	V _{GS} =0V, V _{DS} =0V, f=1MHz	-	-	-	Ω