

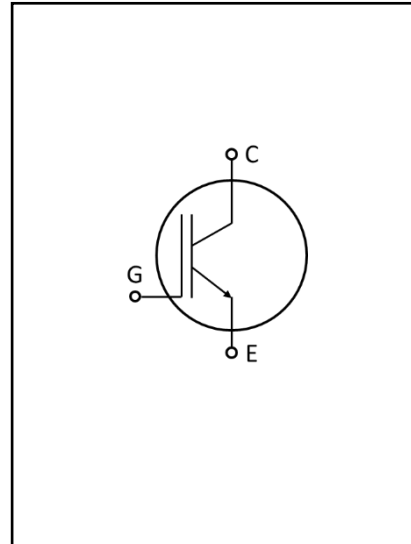
IGBT Chip

Features:

- 600V Trench & Field stop technology
- Low switching losses
- Positive temperature coefficient
- Easy paralleling

Applications:

- Power drives



Mechanical parameters

Die size	9.69×7.87	mm ²
Emitter pad size	See chip drawing	
Gate pad size	1.61×0.81	
Area total	76.26	
Thickness	65	μm
Scribe line Size	80	
Wafer size	200	mm
Max. possible chips per wafer	320	
Passivation front side	Polyimide	
Pad metal	AlCu with Ti/TiN (4.5μm & 400A/1000A)	
Backside metal	Al/Ti/Ni/Ag	

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-Emitter voltage	V_{CE}	600	V
DC collector current	I_C	150	A
Operating junction temperature	T_{vj}	-40 ... +150	°C
Gate emitter voltage	V_{GE}	±20	V
Short circuit data	t_{SC}	10	µs

Static Characteristics, $T_{vj}=25^{\circ}\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			Min.	Typ.	Max.	
Collector-Emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0\text{V}, I_C=1\text{mA}$	600			V
Collector-Emitter saturation voltage	V_{CEsat}	$V_{GE}=15\text{V}, I_C=150\text{A}$		1.80	2.20	
Gate-Emitter threshold voltage	$V_{GE(th)}$	$I_C=2.4\text{mA}, V_{GE}=V_{CE}$	5.1	5.7	6.3	
Zero gate voltage collector current	I_{CES}	$V_{CE}=600\text{V}, V_{GE}=0\text{V}$			10	µA
Gate-Emitter leakage current	I_{GES}	$V_{CE}=0\text{V}, V_{GE}=20\text{V}$			150	nA
Integrated gate resistor	r_G			1.5		Ω
Input capacitance	C_{ies}	$V_{CE}=25\text{V}, V_{GE}=0\text{V},$ $f=1\text{MHz}$		9.0		nF
Reverse transfer capacitance	C_{res}			0.4		

Chip Drawing

